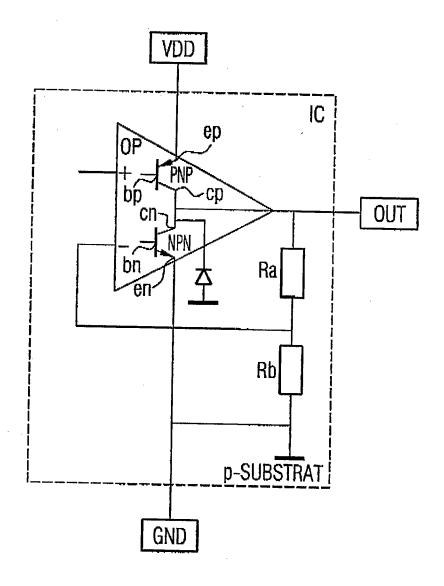
Inventor: Udo Ausserlechner Attorney Docket.: 068758.0195 Replacement Page 7 of 9

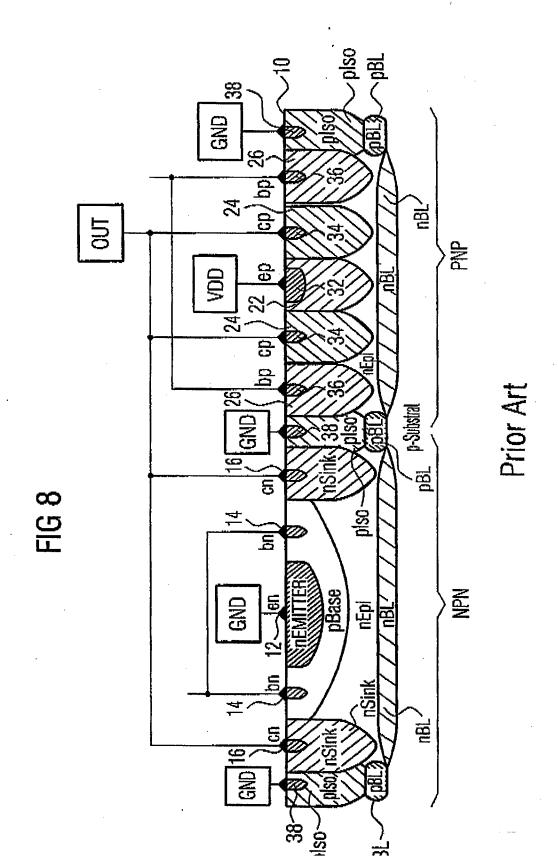
FIG 7



Prior Art

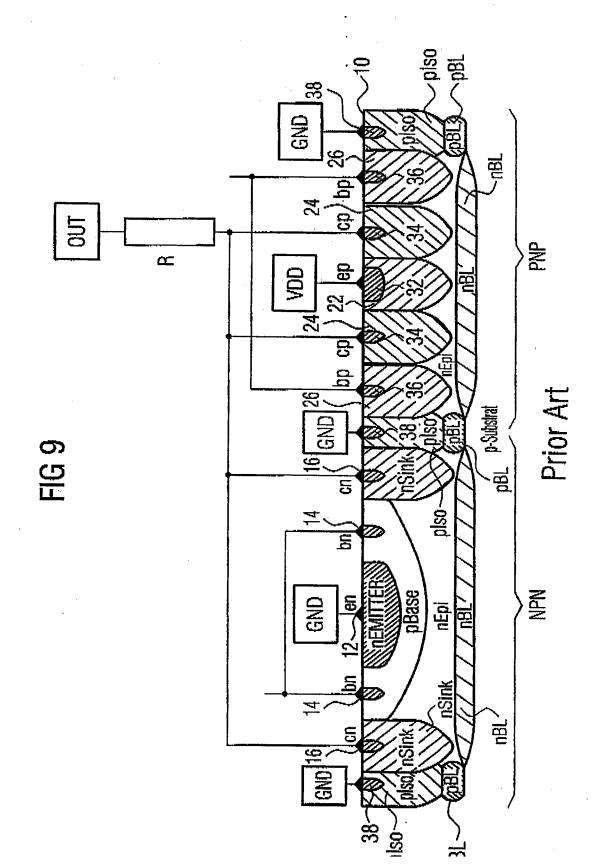
Circuit

Inventor: Udo Ausserlechner Attorney Docket.: 068758.0195 Replacement Page 8 of 9



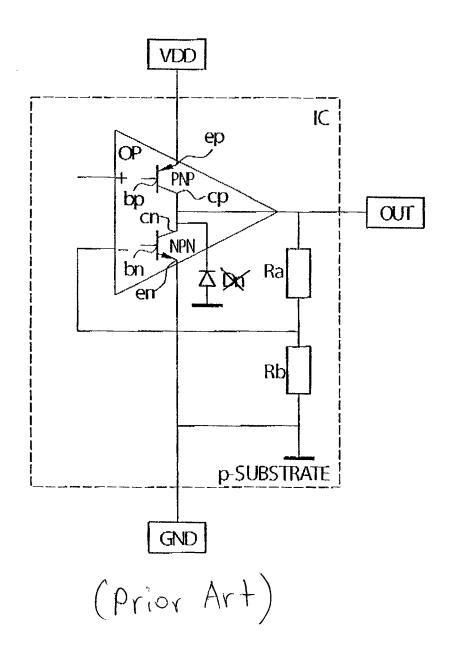
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Inventor: Udo Ausserlechner Attorney Docket.: 068758.0195 Replacement Page 9 of 9



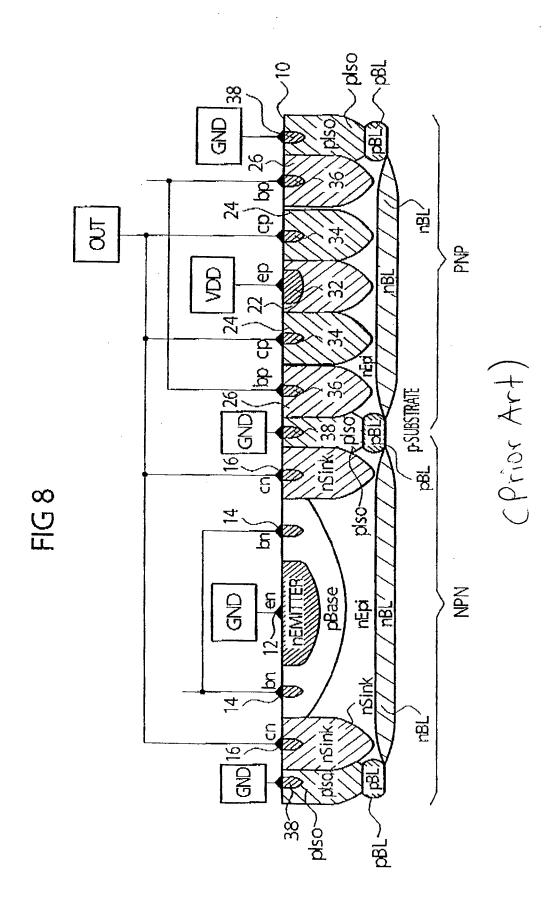
Inventor: Udo Ausserlechner Attorney Docket.: 068758.0195 Annotated Page 7 of 9

FIG 7



Inventor: Udo Ausserlechner Attorney Docket.: 068758.0195

Annotated Page 8 of 9



Inventor: Udo Ausserlechner Attorney Docket.: 068758.0195

Annotated Page 9 of 9

